

Prior Art
Att 3
PATENTS
Serial No.
19-1490

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Yoshiaki YAMAMOTO

Serial No. (unknown)

Filed herewith

METHOD OF FABRICATING SEMICONDUCTOR
DEVICE FOR PREVENTING RISING-UP
OF SILISIDE

jc542 U.S. PTO
09/089666
06/03/98


INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents

Washington, D.C. 20231

Sir:

In compliance with Rules 1.97 and 1.98, and in fulfillment of the duty of disclosure under Rule 1.56, the accompanying documents, copies of which are attached to this statement, are made of record on the enclosed sheet.

A concise explanation of the relevance of these items is that we are advised by our client that these references were discovered during any searches they or their client had made, or that they were considered in the preparation of the application.

Respectfully submitted,

YOUNG & THOMPSON

By



Robert J. Patch
Attorney for Applicant
Registration No. 17,355
745 South 23rd Street
Arlington, VA 22202
Telephone: 703/521-2297

June 3, 1998